AMENDMENTS TO THE CLAIMS

The listing of claims below replaces all prior versions of claims in the application.

1. (Currently Amended): An image processing apparatus taking NXM pixels (N, M being a natural number of 2 or more) as one block, for processing image data consisting of a plurality of blocks by the unit block, said image processing apparatus comprising:

a first resizing means for resizing said image data in a first direction;

a line storage means including at least one 1-line line memory ~~line memory~~ having capacity for storing image data corresponding to one line along the first direction of the image data outputted from the first resizing means; and

a second resizing means for resizing ~~the image data~~ in a second direction intersecting said first direction with using image data of the block to be resized, outputted from said first resizing means ~~in a second direction intersecting the first direction,~~ and image data of a block adjacent to the block to be resized, acquired ~~wherein said second resizing means is formed so as to acquire image data of adjacent block~~ from said line storage means.

2. (Original): The image processing apparatus according to claim 1 further comprising a decoding means for decoding compressed and encoded image data block by block, the image data decoded at the decoding means being subjected to the resizing.

3. (Withdrawn): The image processing apparatus according to claim 1 further comprising an encoding means for compressing and encoding image data block by block, the resized image data being compressed and encoded at said encoding means.

4. (Currently Amended): The image processing apparatus according to claim 1, wherein said first resizing means resizes said image data based on ~~thinning-out~~ thinning-out pixels in the first direction.

5. (Currently Amended): The image processing apparatus according to claim 1, wherein said first resizing means resizes said image data based on an added average of a number of adjacent pixels in the first direction.

6. (Withdrawn): The image processing apparatus according to claim 1 further comprising a pixel storage means capable of storing at least image data corresponding to the number of pixels of block in the second direction of said NXM pixel block, said first resizing means acquiring image data of adjacent block from said pixel storage means.

7. (Withdrawn): The image processing apparatus according to claim 6, wherein said pixel storage means is capable of storing image data corresponding to the number of pixels of block in the second direction of the NXM pixel block, and said first resizing means effects resizing based on 2-point interpolation in the first direction.

8. (Withdrawn): The image processing apparatus according to claim 6, wherein said pixel storage means is capable of storing image data corresponding to three times the number of pixels of block in the second direction of the NXM pixel block, and said first resizing means effects resizing based on 4-point interpolation in the first direction.

9. (Previously Presented): The image processing apparatus according to claim 1, wherein said line storage means comprises a single line memory having capacity for storing image data corresponding to one line in the first direction of the image data resized at said first resizing means, and said second resizing means effects resizing based on 2-point interpolation in the second direction.

10. (Withdrawn): The image processing apparatus according to claim 1, wherein said line storage means is capable of storing image data corresponding to three lines in the first direction of the image data resized at said first resizing means, and said second resizing means effects resizing based on 4-point interpolation in the second direction.

11. (Previously Presented): The image processing apparatus according to claim 1 further comprising a first through resizing means for bypassing a processing of the first resizing means.

12. (Previously Presented): The image processing apparatus according to claim 1 further comprising a second through resizing means for bypassing a processing of the second resizing means.

13. (Previously Presented): The image processing apparatus according to claim 11 further comprising a second through resizing means for bypassing a processing of the second resizing means.

14. (Previously Presented): The image processing apparatus according to claim 1,

wherein said line storage means has the capacity corresponding to a display region of an external display apparatus.

15. (New): An image processing apparatus taking NXM pixels (N, M being a natural number of 2 or more) as one block, for processing image data consisting of a plurality of blocks by the unit block, said image processing apparatus comprising:

a first resizing circuit for resizing said image data in a first direction;

a line storage section including at least one 1-line line memory having capacity for storing image data corresponding to one line along the first direction of the image data outputted from the first resizing circuit; and

a second resizing circuit for resizing in a second direction intersecting said first direction with using image data of the block to be resized, outputted from said first resizing circuit and image data of a block adjacent to the block to be resized, acquired from said line storage section.